Docket No.: 1614.1218

IN THE SPECIFICATION:

The specification as amended below with replacement paragraphs shows added text with <u>underlining</u> and deleted text with <u>strikethrough</u>.

Please REPLACE the paragraph beginning at page 4, line 35, with the following paragraph:

The image recording apparatus includes a tuner 11, which is connected to an antenna 10, an NTSC decoder 12, an audio A-D converter (ADC) 13, an MPEG 2 encoder 14, an MPEG 2 decoder 18, an NTSC encoder 22, and an audio D-A converter (DAC) 23. The tuner 11, NTSC decoder 12, audio A-D converter 13, and MPEG 2 encoder 14 constitute an input system of the image recording apparatus, while the MPEG 2 decoder 18, NTSC encoder 22, and audio D-A converter 23 constitute an output system thereof. An analog image signal from the tuner 11 is given to the NTSC decoder 12 from the outside through an input terminal 29, and an analog audio signal is given to the audio A-D converter 13 through an input terminal 30. An analog image signal which the NTSC encoder 22 outputs is provided to an external apparatus, such as a television set, through an output terminal 31. An analog audio signal which the audio D-A converter 23 outputs is provided to the external apparatus through an output terminal 3132.

Please REPLACE the paragraph beginning at page 5, line 29, with the following paragraph:

The video encoder 15 of the MPEG 2 encoder 14 carries out coding and compressing of the image signal decoded by the NTSC decoder 4512, and outputs it to the multiplexer 17. The compression form of, for example, MPEG 2-Video is used for this coding and compressing. The audio encoder 16 carries out coding and compressing of the digital audio signal output from the audio A-D converter 13, and provides it to the multiplexer 17. The compression form of MPEG 1 Audio Layer 2 is used for this coding and compressing. The multiplexer 17 multiplexes the thus-provided image signal and audio signal, and thus, outputs a stream. For example, an MPEG 2 system PS form is used for this multiplexing.

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Please REPLACE the paragraph beginning at page 13, line 36, with the following paragraph:

Furthermore, the image recording apparatus shown in FIG. 5 has a display unit 138 including a controller 125, an SRAM-SDRAM 126, an ATAPI interface part 127, a HDD 128, input terminals 129, output input terminals 130, a remote controller input part 131, a mute filter part 134, a video amplifier 135, a buffer 136, a CPU bus 137, a liquid crystal device, etc.

Please REPLACE the paragraph beginning at page 14, line 7, with the following paragraph:

The controller 125 has an ATAPI interface part 125a, an encoder DMAC (dynamic memory access controller) 125b, a decoder DMAC 125c, a disk DMAC 125d, a remote controller interface 125e, a serial interface 125f, a stream input interface 125g, an SDRAM interface 125h, a CPU bus interface 125i, a register 125j, and a stream output interface 125h.

Please REPLACE the paragraph beginning at page 15, line 15, with the following paragraph:

The input terminals 129 include a composite terminal CT, a Y terminal, and a C terminal. The input terminals 130 include an L (left) terminal and an R (right) terminal. The output terminals 434–131' include a composite terminal, a Y terminal, and a C terminal. The output terminals 132 include an L (left) terminal and an R (right) terminal.